



The Need for a New I/O Model

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Modeling Performance

<mark>₿</mark>8 DiSC

"Algorithm/Data Structure **X** has O(f(N)) performance, where N is the number of data pages on disk"

... is probably one of the most commonly read phrases in SIGMOD papers.







Small, fast main memory (size M)







Large, slow external memory



Small, fast main memory (size M)





One I/O at a time



Small, fast main memory (size M)









0 access cost

Small, fast main memory (size M)







0 access cost

망요 DiSC

> Small, fast main memory (size M)







total cost \cong total # reads/writes to disk



Small, fast main memory (size M)





Two (outdated) assumptions

Symmetric cost for Read & Write to disk

 $\circ~$ One I/O at a time



Small, fast main memory (size M)





HDD vs. SSD



lab OSIC





Read/Write a page from/to HDD

read or write page 5 -

ि S DisC



Accessing a page from SSD

read page 5



lab Sada DSiO

Navigate to

- Channel
- Chip
- Die
- Plane
- Block



Image Source: Garrett et al. "Enabling Intra-Plane ParallelBlock Erase in NAND Flash to Alleviate the Impact of Garbage Collection"



Accessing a page from SSD

read page 5



lab **S**ad **S**ad

Navigate to

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Opportunities for *concurrently* reading or writing multiple pages

Image Source: Garrett et al. "Enabling Intra-Plane ParallelBlock Erase in NAND Flash to Alleviate the Impact of Garbage Collection"





Out-of-place updates cause invalidation

Invalidation causes garbage collection



Plane





Block 0

lab **S**ad **S**ad

Block 1

Writing in a free page isn't costly!





lap de Calanda





Block 0

lab **S**ad **S**ad

Block 1

Not all updates are costly!





What if there is no space?



. . .

Block 0

Block N

0'

R'





What if there is no space?



Garbage Collection!



. . .



Block N

N

Q

0'

R'



What if there is no space?

lab Sada DSiO



Garbage Collection!





<u>क</u> <u>व</u> DisC



Higher average update cost (due to GC) \rightarrow *Read/Write asymmetry*



Measuring Asymmetry/Concurrency in off-the-shelf SSD

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Measuring Asymmetry/Concurrency in NVMe SSD







Make asymmetry and concurrency part of algorithm design

... not simply an engineering optimization

Build algorithms/data structures for storage devices with asymmetry α and concurrency k

index structures



graph traversal algorithms

bufferpool management





Asymmetry & Concurrency Aware Bufferpool Strategy

exploit device parallelism

concurrent write-back without evicting

bridge read/write asymmetry

SSD with asymmetry: 1.5x & concurrency: 9

Speedup on TPCC vs. LRU





exploit device parallelism

concurrent write-back without evicting

bridge read/write **asymmetry**

Thank you!

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DiS

SSD with asymmetry: 1.5x & concurrency: 9

Speedup on TPCC vs. LRU

