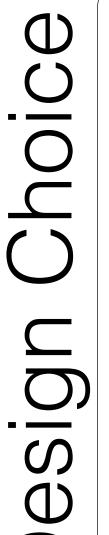
Constructing and Analyzing the LSM Compaction Design Space

Dimitris Staratzis Subhadeep Sarkar Zichen Zhu Manos Athanassoulis

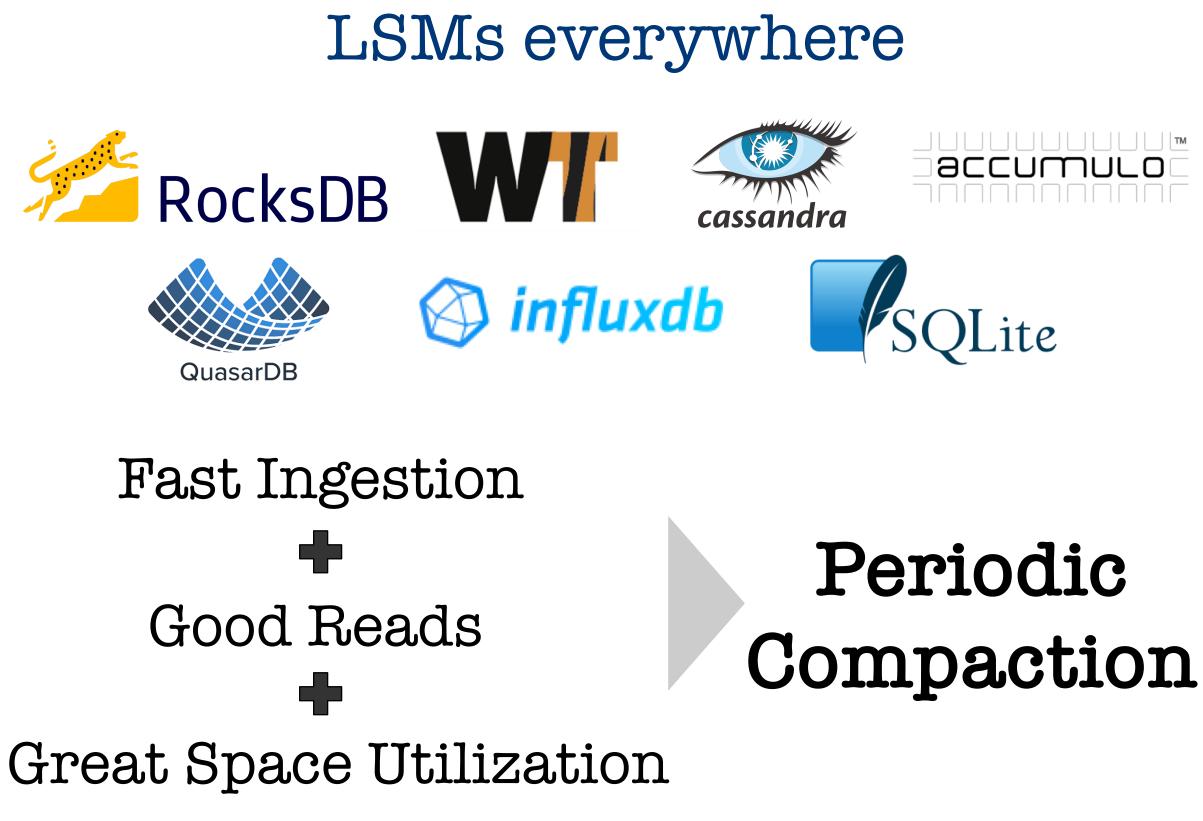


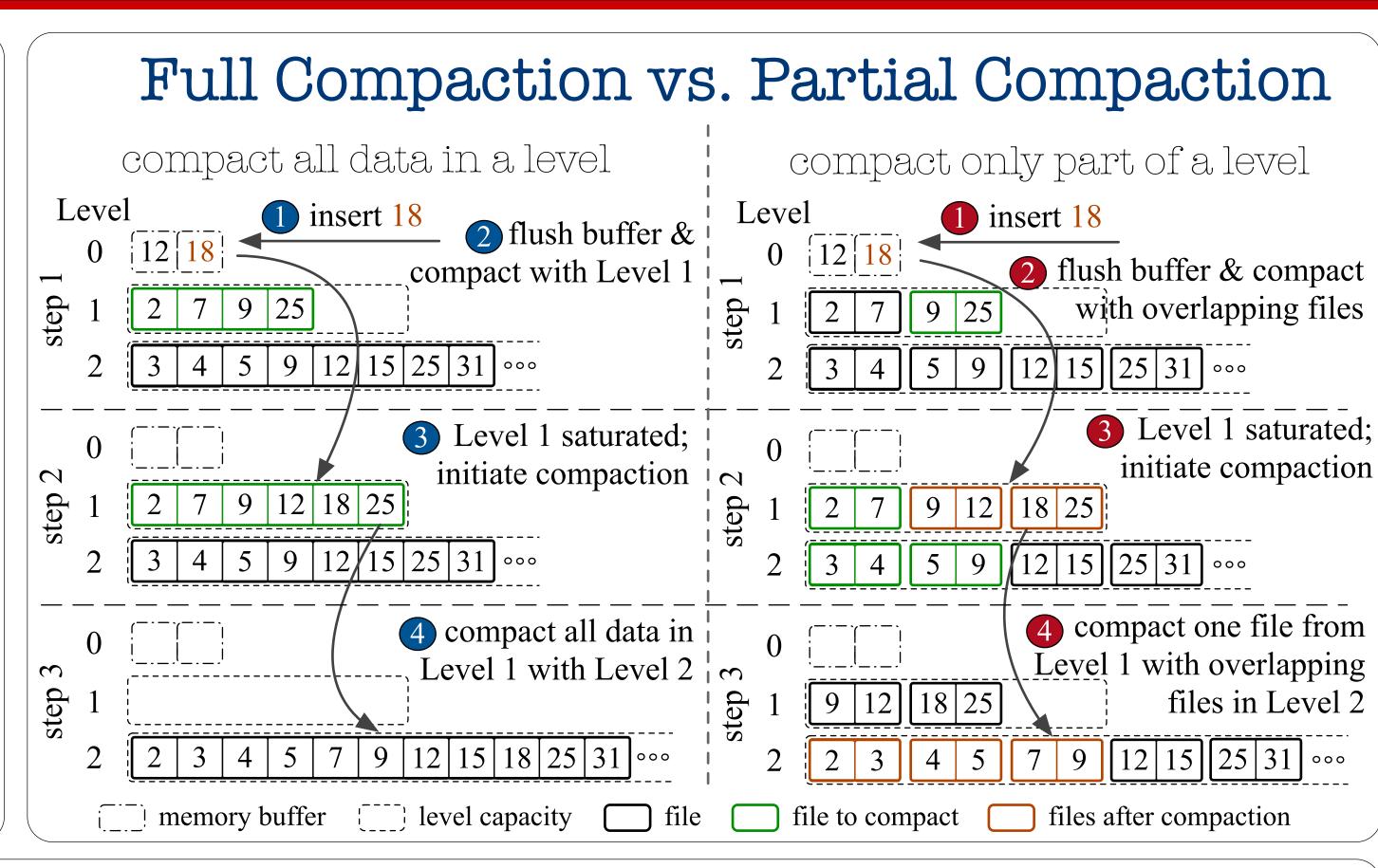


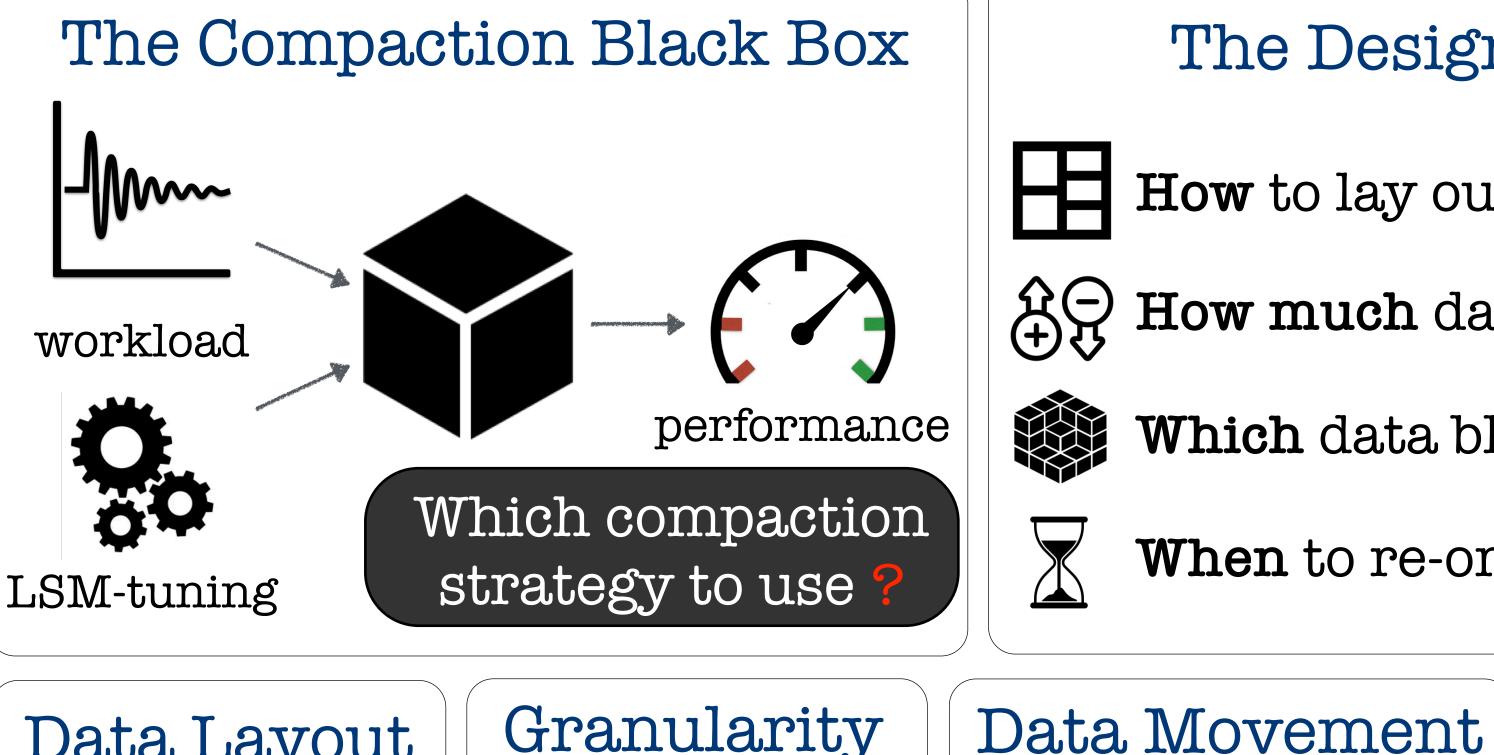




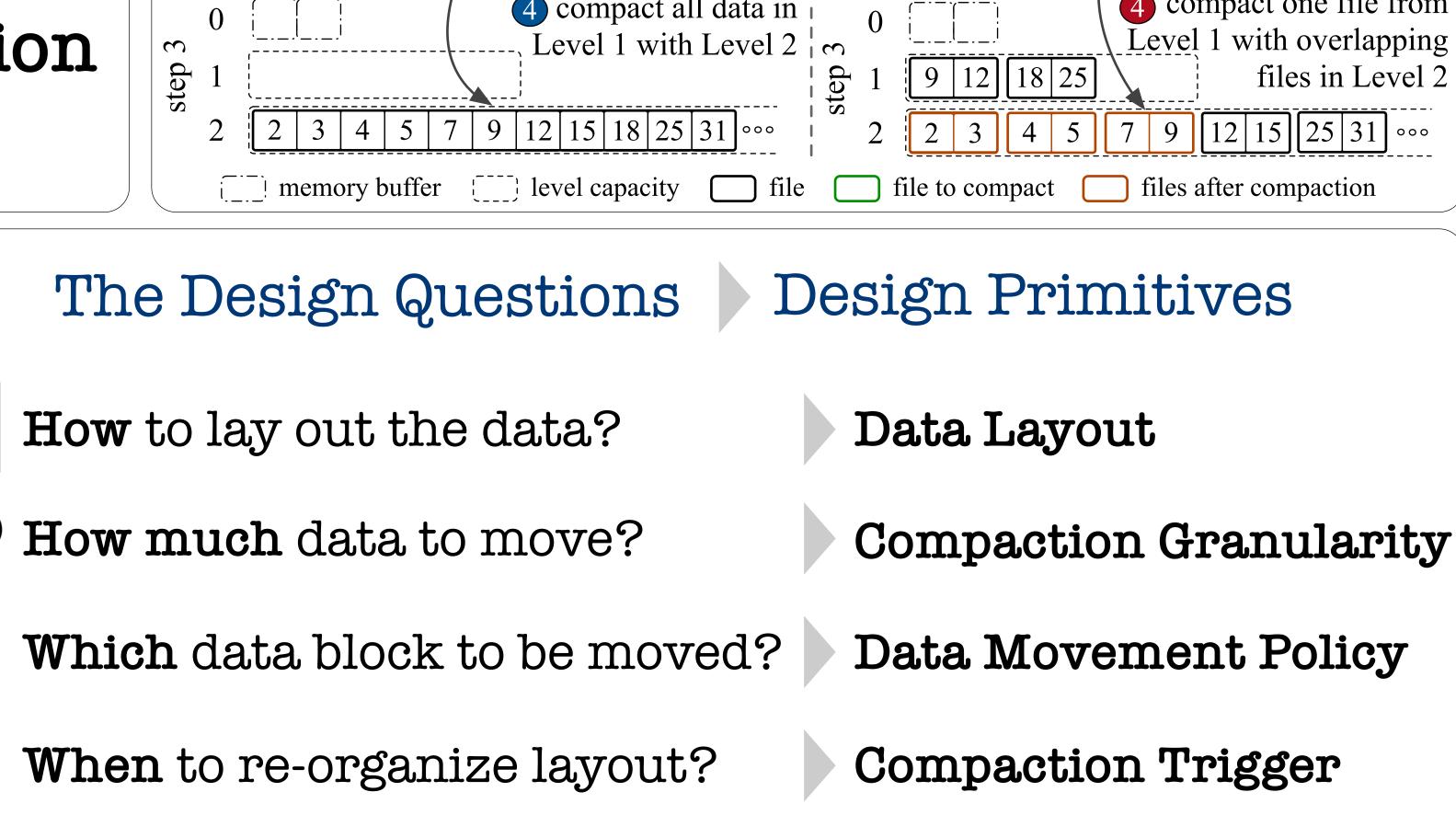


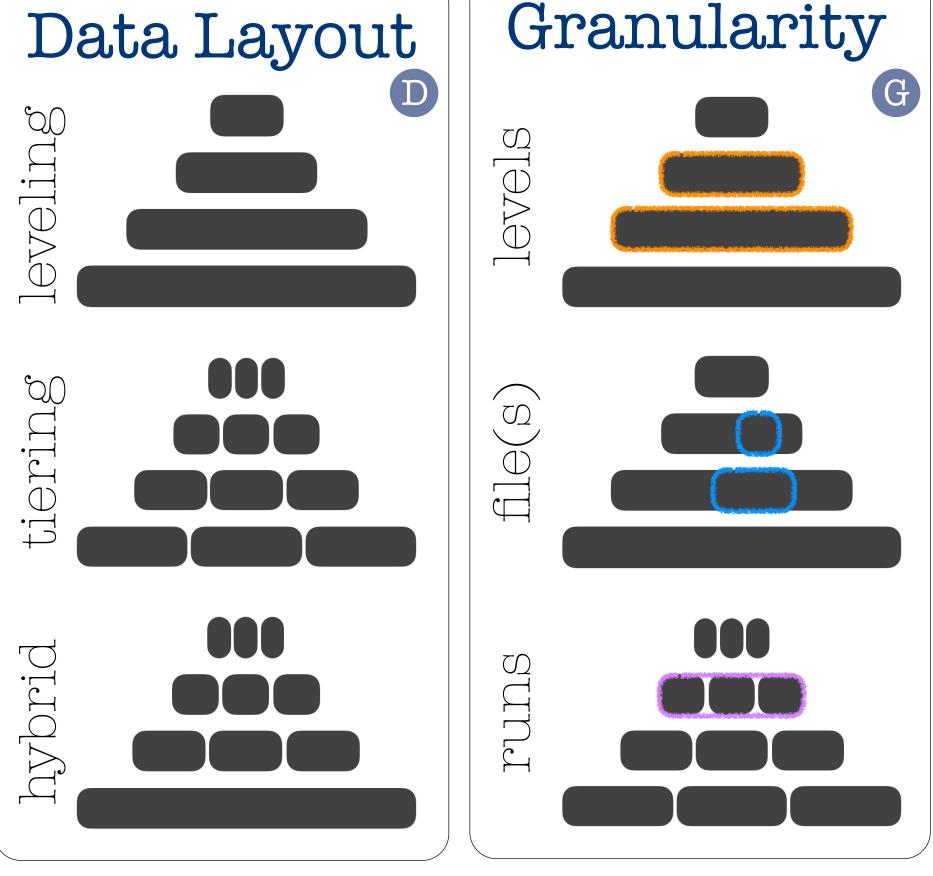






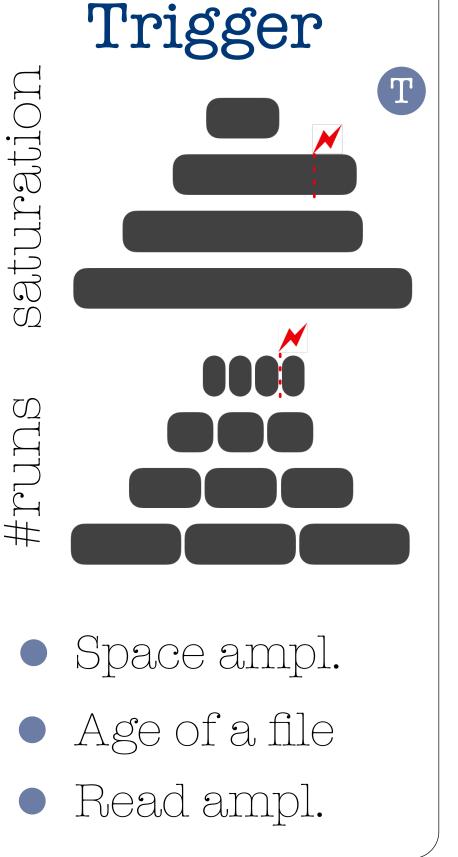


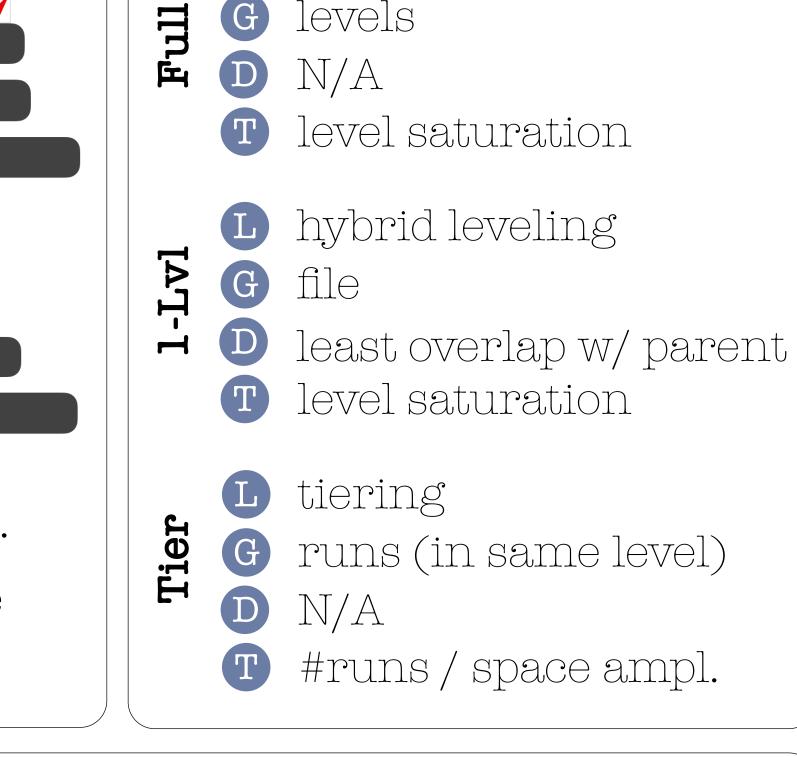






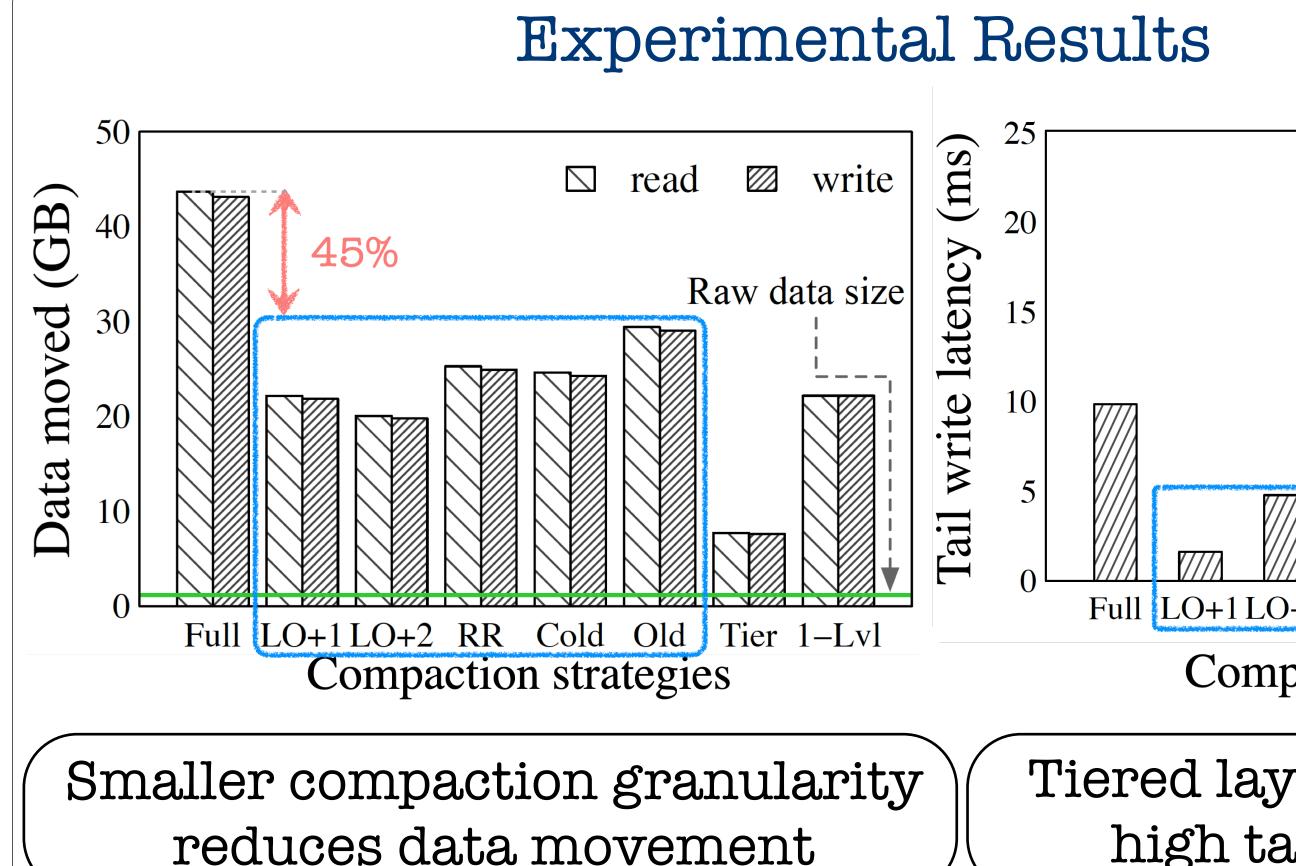
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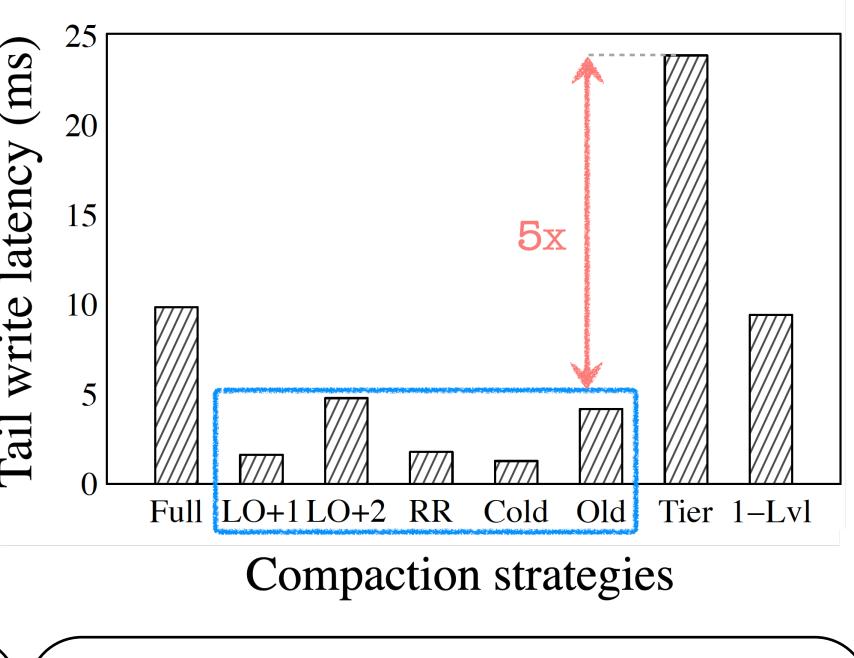




Primitive Ensemble

leveling





Tiered layout has extremely high tail write latency

Key Takeaways

Know your LSM-compaction

Avoid the worst designs

Adapt choices w/ workloads

Design new compactions