

Lecture Notes 13:**Karp-Lipton, Circuit Lower Bounds, Restricted Circuit Classes****Reading.**

- Arora-Barak § 6.4-6.7

Last time: Intro to Circuits

A circuit family $C = \{C_n\}_{n=1}^{\infty}$ is an infinite sequence of circuits, where each $C_n : \{0, 1\}^n \rightarrow \{0, 1\}$ is built from \wedge, \vee, \neg gates.

The class \mathbf{P}/poly consists of languages that are decided by poly-size circuit families.

1 Relating Circuits and TMs

The key conceptual difference between Turing machines and circuits is that the former is a uniform model of computation (i.e., the same algorithm is used on inputs of every length), whereas the latter is a non-uniform model (i.e., a different algorithm is used on each input length.) In a sense, this is the only difference, as efficient TMs are equivalent to uniformly-generated circuit families, while arbitrary circuit families are equivalent to TMs that take “non-uniform advice.”

Last time, we saw the first of these statements:

Theorem 1. *A language $L \in \mathbf{P}$ if and only if there exists a logspace uniform circuit family computing L .*

Now let’s see how to “non-uniformize” Turing machines to get an equivalent characterization of \mathbf{P}/poly .

Theorem 2. *A language $L \in \mathbf{P}/\text{poly}$ if and only if L is decided in poly-time by a TM taking “polynomial advice.” That is, there exists a TM M , a polynomial p , and an infinite sequence of strings $\{\alpha_n\}_{n=1}^{\infty}$, with $\alpha_n \in \{0, 1\}^{p(n)}$ such that $x \in L \iff M(x, \alpha_{|x|}) = 1$.*

Proof. For the “only if” direction, let $L \in \mathbf{P}/\text{poly}$ be computed by a poly-size circuit family $\{C_n\}$. Define an advice sequence by $\alpha_n = \lfloor C_n \rfloor$. Then the following TM M with advice $\{\alpha_n\}$ decides L in poly-time:

On input x, α_n :

1. Construct circuit C_n corresponding to α_n
2. Evaluate $C_n(x)$.

For the “if” direction, let L be decided by a poly-time TM M using advice $\{\alpha_n\}$. For each n , use the transformation described in the proof of Lecture 12, Theorem 6 on $M(\cdot, \alpha_n)$ to obtain a poly-size circuit family. \square

2 Karp-Lipton Theorem

Non-uniform advice bears a tantalizing similarity to the witnesses/certificates used to characterize **NP** languages. But the order of quantifiers makes a big difference! Specifically, a machine M decides a language L in each model if:

Non-uniform advice: $\exists\{\alpha_n\}_{n=1}^\infty \forall x \in \{0, 1\}^* \quad x \in L \iff M(x, \alpha_n) = 1$

Certificates: $\forall x \in \{0, 1\}^* \quad x \in L \iff \exists w M(x, w) = 1.$

That is, with non-uniform advice, the same advice string α_n has to work for all strings x of length n , whereas an NP certificate is allowed to depend on the particular instance x . At first glance, this may make non-uniform advice appear less powerful than NP certificates. But the upshot of the quantifier switch is that the same advice string equally helps certify non-membership in L . So the resources seem incomparable.

In fact, we already know that non-uniform advice can be more powerful than nondeterminism since the former can be used to solve undecidable problems. Thus, $\mathbf{P}_{/\text{poly}} \not\subseteq \mathbf{NP}$. What about the other direction?

Theorem 3 (Karp-Lipton). *If $\mathbf{NP} \subseteq \mathbf{P}_{/\text{poly}}$, then $\mathbf{PH} = \Sigma_2^p$.*

Before proving this, let's mention some interpretations:

- Taking the contrapositive, the Karp-Lipton Theorem says that if the polynomial hierarchy does not collapse, then $\mathbf{NP} \not\subseteq \mathbf{P}_{/\text{poly}}$. So this can be taken as evidence that **NP** does not have poly-size circuits.
- Combined with the observation that $\mathbf{P}_{/\text{poly}} \not\subseteq \mathbf{NP}$, this suggests that non-uniform advice and non-determinism are incomparable computational resources.
- Given this evidence that $\mathbf{NP} \not\subseteq \mathbf{P}_{/\text{poly}}$, a viable strategy for proving that $\mathbf{NP} \not\subseteq \mathbf{P}$ would be to actually prove the *stronger* statement that, say, SAT does not have poly-size circuits. Since circuits are nice combinatorial objects, this might be easier to prove than trying to reason about the TM complexity of SAT directly.

Proof. Suppose $\mathbf{NP} \subseteq \mathbf{P}_{/\text{poly}}$. To show that $\mathbf{PH} = \Sigma_2^p$, it suffices to show that $\mathbf{PII}_2^p \subseteq \Sigma_2^p$. To establish this, it in turn suffices to show that the \mathbf{PII}_2^p -complete problem $\Pi_2\text{-SAT} \in \Sigma_2^p$. That is, we want to exhibit a Σ_2 -type ATM determining the truth of formulas of the form “ $\forall u \exists v \varphi(u, v)$.”

The idea is as follows. Our assumption that $\mathbf{NP} \subseteq \mathbf{P}_{/\text{poly}}$ means that for every u , the subproblem $\exists v \varphi(u, v)$ can be computed by a small circuit C . Our Σ_2 machine will *first* existentially guess this circuit, then use universal guessing to check it.

So now let's carry out this idea in detail. We need to do a bit of technical preparation. First, recall the search-to-decision reduction for SAT, which says that if $\text{SAT} \in \mathbf{P}$, then there is a poly-time algorithm that finds a satisfying assignment whenever one exists. This transformation works equally well for circuits. That is, if $\text{SAT} \in \mathbf{P}_{/\text{poly}}$, there exists a poly-size circuit family $\{C_{\text{SAT}, n}\}_{n=1}^\infty$ such that $C_{\text{SAT}, |\psi|}(\psi)$ outputs a satisfying assignment to ψ whenever one exists.

Let's also introduce the following piece of helpful notation. For a formula $\varphi(u, v)$, and a partial assignment a to the variables u , define $\varphi_{u=a}(v)$ to be the formula obtained by setting the variables u to assignment a . Now consider the following Σ_2 -type TM:

On input a quantified formula $\forall u \in \{0, 1\}^n \exists v \in \{0, 1\}^n \varphi(u, v)$:

1. Existentially guess a circuit C (representing a guess for C_{SAT} of appropriate size to handle as input a formula $\varphi_{u=a}$)
2. Universally guess a string $a \in \{0, 1\}^n$
3. Construct the formula $\varphi_{u=a}(v)$. Use the circuit to produce a candidate satisfying assignment $b = C(\varphi_{u=a})$.
4. Check that $\varphi(a, b) = 1$.

To see why this is correct, observe that

$$\begin{aligned} \forall u \in \{0, 1\}^n \exists v \in \{0, 1\}^n \varphi(u, v) &\iff \forall a \in \{0, 1\}^n \varphi_{u=a} \in \text{SAT} \\ &\iff \forall a \in \{0, 1\}^n \varphi(a, C_{\text{SAT}}(\varphi_{u=a})) = 1 \\ &\iff \exists C \forall a \in \{0, 1\}^n \varphi(a, C(\varphi_{u=a})) = 1, \end{aligned}$$

which is the criterion that our ATM checks. □

3 Circuit Lower Bounds

Despite the fact that $\mathbf{P}_{/\text{poly}}$ contains undecidable languages, the Karp-Lipton Theorem says it's reasonable to conjecture that $\mathbf{NP} \not\subseteq \mathbf{P}_{/\text{poly}}$. So why might it be easier to prove this statement than to prove that $\mathbf{NP} \not\subseteq \mathbf{P}$ directly? The answer is that we have techniques beyond diagonalization and related arguments for proving circuit lower bounds, in particular, techniques that don't relativize.

Here's a simple (but dramatic) example of a circuit lower bound.

Theorem 4. *For every (sufficiently large) n , there exists a function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ that requires circuit size $\geq 2^n/5n$.*

Note that this is essentially tight. One can show that every function is computed by a circuit of size $O(2^n/n)$. Actually, we have a very sharp understanding of the maximal circuit complexity of n -input functions; it's between $2^n/n$ and $(1 + o(1))2^n/n$.

Proof. The proof is by a "counting argument." We're going to count the number of functions $f : \{0, 1\}^n \rightarrow \{0, 1\}$, and count the number of small circuits, and show that the former is larger than the latter.

1. How many n -bit functions $f : \{0, 1\}^n \rightarrow \{0, 1\}$ are there? Answer: 2^{2^n} .
2. How many (fan-in 2) circuits of size s are there? For each vertex $v = 1, \dots, s$, there are:
 - $\leq s^2$ ways to determine the inputs to v
 - $\leq n + 3$ choices for what type of vertex v is (n inputs, \wedge , \vee , \neg)

Thus, there are at most $(s^2(n + 3))^s \leq 2^{4s \log s}$ circuits of size s .

If $s \leq 2^n/5n$, then we have

$$2^{4s \log s} \leq 2^{4(2^n/5n) \cdot n} \leq 2^{2^n \cdot (4/5)} < 2^{2^n}.$$

□

In fact, note that the ratio of the number of circuits to the number of functions is at most

$$\frac{2^{2^n \cdot (4/5)}}{2^{2^n}} = 2^{-2^n/5} \rightarrow 0$$

as $n \rightarrow \infty$, so “almost all” functions do not have circuits below this size.

4 Restricted Depth Classes

We know that most functions require huge circuits, but we’d really like to show that functions we care about (e.g., SAT, or any language in NP) do as well.

Sadly, we do not yet have good techniques for proving general circuit lower bounds for “explicit” functions. But we do have techniques for studying circuits of small depth.

Definition 5. For each natural number k , define NC^k to be the class of languages decidable by circuits of size $\text{poly}(n)$ and depth $O(\log^k n)$.

The class $\text{NC} = \bigcup_{k=1}^{\infty} \text{NC}^k$.

(NC stands for “Nick’s Class” named after Nicholas Pippenger.)

Definition 6. For each natural number k , define AC^k to be the class of languages decidable by circuits of size $\text{poly}(n)$ and depth $O(\log^k n)$ using unbounded fan-in \wedge and \vee gates.

The class $\text{AC} = \bigcup_{k=1}^{\infty} \text{AC}^k$.

(AC stands for “Alternating Class” after the connection to alternating TMs.)

Fact 7. For every k , we have $\text{NC}^k \subseteq \text{AC}^k \subseteq \text{NC}^{k+1}$.

Proof. The first inclusion is automatic from the definitions. For the second inclusion, we use the fact that any gate with $\text{poly}(n)$ fan-in can be replaced by an $O(\log n)$ -depth tree. \square

These classes are already interesting (and test the limits of our understanding) at low levels.

NC⁰: This class consists of languages decidable by constant-depth circuits. So observe that each output bit can only depend on a constant number of input bits.

This isn’t very interesting for languages, but for multi-bit output functions, there is evidence that this class is powerful enough to do cryptography. (See [Appelbaum-Ishai-Kushilevitz06], “Cryptography in NC⁰”.)

AC⁰: This class generalizes DNF and CNF (depth-2) to circuit families of arbitrary constant depth. Some basic arithmetic (addition, but not multiplication), approximate counting, and searching constant-width mazes are possible in AC⁰. Some important examples of functions not computable in AC⁰ are parity and majority.

NC¹: This class is equivalent to poly-size formulas (circuits where each gate has fan-out 1). This is because formulas can be “balanced” recursively to bring their depth down to $O(\log(\text{size}))$. Sadly, the best known formula size bound for an explicit function is only about n^3 .

4.1 Parallel Computation

Roughly, (uniform) circuits of size s and depth d capture what can be computed by s parallel, random-access processors running in “wall-time” d .

Therefore, (uniform) NC captures the languages decidable by a parallel computer with polynomially many processors running in polylogarithmic time.